

**AMENDMENTS TO THE SPECIFICATION:**

*Please replace paragraphs [0004]-[0005] with the following amended paragraphs:*

[0004] FIG. 1 is a view schematically showing a driving apparatus of a ~~general related art~~ PDP, FIG. 2 is a detailed view of the driving part of the driving apparatus of the PDP of FIG. 1, and FIG. 3 is a detailed view of the control board in the driving apparatus of the PDP of FIG. 1.

[0005] Referring to FIGS. 1 to 3, the driving apparatus of a ~~general related art~~ PDP includes an interface board 11 for receiving a TV/PC video signal and a synchronous signal, an AC-DC converter 12 for converting an AC signal into a DC signal, and a PDP module 15 for controlling a PDP as a whole, based on the video signal and the synchronous signal.

*Please replace paragraphs [0026]-[0027] with the following amended paragraphs:*

[0026] Furthermore, since the respective frame memories are individually mounted, the length of the signal lines for connecting the respective frame memories is lengthened and accordingly, the signal lines are formed close as well. As a result, inductance formed between the adjacent signal lines is increased and thus the signal property of the frame memory is deteriorated. In other words, the control board 13 of the PDP according to the ~~conventional related art~~ has a disadvantage in that the signal property is deteriorated due to the inductance increase between the signal lines connecting between the system control chips 26 and the frame memories 33.

[0027] Moreover, the driving module of the ~~conventional related art~~ PDP is fabricated in a considerably large size due to the frame memories 33 formed around the system control chips

26, which causes a drawback incapable of decreasing the size of the driving system of the PDP and of keeping in pace with the recently requested compactness of the PDPs.

*Please replace paragraph [0039] with the following amended paragraph:*

[0039] FIG. 1 is a schematic view of a ~~general-related art~~ driving apparatus of a plasma display panel;

*Please replace paragraph [0049] with the following amended paragraph:*

[0049] FIG. 5 is a detailed view of a control board in a driving apparatus of a plasma display panel according to a preferred embodiment of the present invention. The driving apparatus of the plasma display panel according to the present invention is similar to that of FIG. 1. However, it is noted that the invention is characterized by a multi-chip module (hereinafter referred to as "MCM") for performing the function of the ~~conventional-related art~~ timing controller, and a fabrication method of such a multi-chip module package. Hereinafter, wherever possible, the same reference numbers will be used throughout the drawings to refer to the same or like parts.

*Please replace paragraph [0061] with the following amended paragraph:*

[0061] The MCM mounted on the control board 13 according to the present invention is fabricated such that a plurality of system control chips and a plurality of frame memories are mounted on a single package. That is, in the ~~conventional-related art~~, the system control chip and the frame memory are separately packaged and mounted on the control board, while in the present invention, the plurality of system control chips and frame memories are fabricated on the MCM 62 as one package. Accordingly, the control board of the PDP according to a

preferred embodiment of the present invention can be reduced to half in its size as compared with the conventional related art control board.

*Please replace paragraph [0075] with the following amended paragraph:*

[0075] As aforementioned, the system control chip 83, the frame memory 86 and other electronic elements 82 are mounted on the MCM package 78. The MCM package 78 has a fabrication process similar to that of the BGA (Ball Grid Package), and is fabricated in a size that is the same as that of the ASIC that is a BGA package used in the control board of the conventional related art PDP. Accordingly, the plurality of system control chip 83, the plurality of frame memories 86 and other several electronic parts are mounted on the conventional related art BGA package size, thereby reducing the size of the control board to 1/2 of the size of the conventional related art control board.

*Please replace paragraph [0081] with the following amended paragraph:*

[0081] As described previously, a driving apparatus of the present invention mounts the system control chip and the frame memory on a single package, thereby reducing the size of the control board to 1/2 of the size of the control board in the conventional related art PDP.

*Please replace paragraph [0083] with the following amended paragraph:*

[0083] Further, according to the PDP driving apparatus of the invention, the size of the PCB of the control board is reduced to 50% of the size of the PCB of the conventional related art control board and the electrical characteristics of the signal lines are enhanced, so that the generation of electromagnetic waves is decreased.